IN THE CLAIMS

Please amend the claims as follows:

1. (currently amended) A method for providing display control on ~~applicable within~~ a computer ~~display~~ system ~~for providing display independence between~~ having a first display device and a second display device, ~~wherein said first and second display devices are controlled by a common video display controller,~~ said method comprising:

allocating ~~providing~~ a first ~~address register~~ memory location for storing contents to be displayed by said first display device, wherein said first memory location ~~that~~ is accessible by ~~said common~~ a video display controller ~~to display within said first display device a graphic representation of data pointed to by an address within said first address register~~; ~~and~~

allocating ~~providing~~ a second ~~address register~~ memory location for storing contents to be displayed by said second display device, wherein said second memory location ~~that~~ is accessible by said ~~common~~ video display controller; ~~to display within said second display device a graphic representation of data pointed to by an address within said second address register~~

in response to a selection of a concurrent display mode, providing identical information to said first and second memory locations, such that contents displayed on said first display device are identical to contents displayed on said second display device; and

in response to a selection of a split display mode, retaining information in said first memory location and updating information in said second memory location, such that contents displayed on said first display device are different from contents displayed on said second display device ~~displays within said first and second display devices are independently controllable~~.

2. (currently amended) The method of claim 1, wherein said providing identical information further ~~comprising: allocating a first frame buffer; and selecting a dual display mode, and in response thereto, programming said first and second address registers to point to said first frame buffer~~ includes providing information from a frame buffer to said first and second memory locations.

3. (currently amended) The method of claim 2, wherein said updating information further includes

allocating a second frame buffer; and

providing information from said second frame buffer to said second memory location while providing information from said frame buffer to said first memory location ~~programming said first and second address registers to point to said first frame buffer during a dual display mode is followed by, in response to said first and second address registers pointing to said first frame buffer, displaying video data from said first frame buffer within said first and second display devices.~~

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4. (currently amended) The method of claim 1, wherein said providing identical information further ~~comprising: selecting a split display mode, and in response thereto: allocating a second frame buffer; copying the contents of said first frame buffer to said second frame buffer; and replacing the contents of said second address register to point to said second frame buffer~~ includes setting a pointer pointing from a frame buffer to said first and second memory locations.

5. (currently amended) The method of claim 4, wherein said updating information further includes

allocating a second frame buffer; and

setting a second pointer pointing from said second frame buffer to said second memory location and setting said pointer pointing from said frame buffer to said first memory location ~~in response to said copying the contents of said first frame buffer to said second frame buffer and adjusting said second address register to point to said second frame buffer, said method further comprises: delivering video data corresponding to the contents of said first frame buffer to said first display device; and delivering video data corresponding to the contents of said second frame buffer to said second display device.~~

6. (currently amended) The method of claim 4 1, wherein said ~~second frame buffer currently stores a display frame, said method further comprising: selecting an alternate display frame within a video memory device; and actuating a static display mode, and in response thereto: maintaining said display frame within said second frame buffer; and copying said alternate display frame within said first frame buffer~~ first display device is external from said computer system and said second display device is internal to said computer system.

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7. (currently amended) The method of claim 4 1, wherein said ~~computer display system includes a display sequence comprising a plurality of display frames within a video memory device, said method further comprising: selecting an M^{th} display frame from within said display sequence; actuating a split sequence display mode; setting a sequence displacement value equal to N; and in response to said actuating a split sequence display mode and setting a sequence displacement value equal to N: copying said M^{th} display frame into said first frame buffer; and copying an $(M-N)^{th}$ display frame into said second frame buffer~~ selection between said concurrent display mode and said split display mode are made via a soft key function.

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Please cancel Claim 8-23.

Please add Claims 24-37 as follows:

24. (new) A computer program product for providing display control on a computer system having a first display device and a second display device, said computer program product comprising:

program code means for allocating a first memory location for storing contents to be displayed by said first display device, wherein said first memory location is accessible by a video display controller;

program code means for allocating a second memory location for storing contents to be displayed by said second display device, wherein said second memory location is accessible by said video display controller;

program code means for providing identical information to said first and second memory locations, in response to a selection of a concurrent display mode, such that contents displayed on said first display device are identical to contents displayed on said second display device; and

program code means for retaining information in said first memory location and updating information in said second memory location, in response to a selection of a split display mode, such that contents displayed on said first display device are different from contents displayed on said second display device.

25. (new) The computer program product of claim 24, wherein said program code means for providing identical information further includes program code means for providing information from a frame buffer to said first and second memory locations.

26. (new) The computer program product of claim 25, wherein said program code means for updating information further includes

program code means for allocating a second frame buffer; and

program code means for providing information from said second frame buffer to said second memory location while providing information from said frame buffer to said first memory location.

27. (new) The computer program product of claim 24, wherein said program code means for providing identical information further includes program code means for setting a pointer pointing from a frame buffer to said first and second memory locations.

28. (new) The computer program product of claim 27, wherein said program code means for updating information further includes

program code means for allocating a second frame buffer; and

program code means for setting a second pointer pointing from said second frame buffer to said second memory location and setting said pointer pointing from said frame buffer to said first memory location.

29. (new) The computer program product of claim 24, wherein said first display device is external from said computer system and said second display device is internal to said computer system.

30. (new) The computer program product of claim 24, wherein said selections between said concurrent display mode and said split display mode are made via a soft key function.

31. (new) An apparatus for providing display control on a computer system having a first display device and a second display device, said apparatus comprising:

means for allocating a first memory location for storing contents to be displayed by said first display device, wherein said first memory location is accessible by a video display controller;

means for allocating a second memory location for storing contents to be displayed by said second display device, wherein said second memory location is accessible by said video display controller;

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means for providing identical information to said first and second memory locations, in response to a selection of a concurrent display mode, such that contents displayed on said first display device are identical to contents displayed on said second display device; and

means for retaining information in said first memory location and updating information in said second memory location, in response to a selection of a split display mode, such that contents displayed on said first display device are different from contents displayed on said second display device.

32. (new) The apparatus of claim 31, wherein said means for providing identical information further includes means for providing information from a frame buffer to said first and second memory locations.

33. (new) The apparatus of claim 32, wherein said means for updating information further includes

means for allocating a second frame buffer; and

means for providing information from said second frame buffer to said second memory location while providing information from said frame buffer to said first memory location.

34. (new) The apparatus of claim 31, wherein said means for providing identical information further includes means for setting a pointer pointing from a frame buffer to said first and second memory locations.

35. (new) The apparatus of claim 34, wherein said means for updating information further includes

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means for allocating a second frame buffer; and

means for setting a second pointer pointing from said second frame buffer to said second memory location and setting said pointer pointing from said frame buffer to said first memory location.

36. (new) The apparatus of claim 31, wherein said first display device is external from said computer system and said second display device is internal to said computer system.

37. (new) The apparatus of claim 31, wherein said selections between said concurrent display mode and said split display mode are made via a soft key function.